




Dayananda Sagar Academy of Technology & Management

(Autonomous Institute under VTU)

Affiliated to **VTU**
Approved by **AICTE**
Accredited by **NAAC** with **A+** Grade
6 Programs Accredited by **NBA**
(CSE, ISE, ECE, EEE, MECH, CV)

Faculty Name	SANDEEP Y	
Academic Designation	Assistant Professor	
Educational Qualification	Ph.D (Pursuing)	
Experience in Teaching & Industry	05	
Date of Joining	21/07/2025	
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Educational Details

- Pursuing Ph.D. (Thesis Submitted) in Vehicular Ad-hoc Networks by Using Nature-Inspired Strategies to Support ITS in Vellore Institute of Technology (VIT), Vellore, Tamil Nadu.
- Completed Master of Technology in Wireless Communication and Sensor Networks specialization, from K L UNIVERSITY with aggregate of 7.9 CGPA.
- Completed Bachelor of Engineering in E.C.E, from S.N.C.E.T affiliated to Anna University Chennai, with an aggregate of 64%.
- Intermediate Education from Vikas Junior College, Tirupathi with an aggregate of 79.3%
- Secondary School Education from Satya Sai E.M High School, Madanapalle with an aggregate of 63%.

Professional Experience

- Worked as an Assistant professor Junior, in the Dept. of Embedded Technology, School of Electronics Engineering at Vellore Institute of Technology (VIT), Vellore, from April 2021 to May 2024.
- Worked as an Assistant professor, in the Dept. of Electronics & Communication Engineering, at Mother Theresa Institute of Engineering and Technology, Palamaner, from June 2016 to May 2019.

Publications

- Sandeep Y and Venugopal P, “Optimal data dissemination by using meta-heuristic strategy over 802.11p in VANETs to improve efficiency and effectiveness”, is published in Results in Engineering, Elsevier, 2025 with (Impact Factor: 6.0) DOI: 10.1016/j.rineng.2024.103830.
- Sandeep Y and Venugopal P, “Enhancing Network Stability in VANETs Using Nature-Inspired Algorithm for Intelligent Transportation System”, is published in PLOS ONE, 2024 with (Impact Factor: 3.0), DOI: 10.1371/journal.pone.0296331.
- Sandeep Y and Venugopal P, “A Swarm Intelligent–Based Cluster Optimization in Vehicular Ad Hoc Networks for ITS”, is published in International Journal of Communication Systems with (Impact Factor: 1.7), DOI: 10.1002/dac.70016.
- P.V.V. Kishore, K.V.V. Kumar, D. Anil Kumar, M.V.D. Prasad, E.N.D. Goutham, R. Rahul, C.B.S. Vamsi Krishna, Sandeep Y, “Twofold processing for denoising ultrasound medical images”, is published in SPRINGERPLUS in 2015.DOI: 10.1186/s40064-015-1566-6.
- Sandeep Y and MD. Ali Hussain, “Implementing Privacy Homomorphism in Data Aggregation for Wireless Sensor Networks”, is published in Indian Journal of Science and Technology in 2017. DOI: 10.17485/ijst/2017/v10i4/110668.

International Conferences: NA

- Sandeep Y and Venugopal P “In-depth Evaluation of Security Requirements and Attacks for Secure Data Communication In ITS”, is published in Second International Conference on Automation, Signal Processing, Instrumentation and Control (ICASIC)-2022. DOI: 10.1063/5.0189841.

National Conferences:

NA

Conference Attended

NA

Awards

- I was awarded with “RAMAN RESEARCH AWARD” by VIT, Vellore, on be-half of my Research article published in Scopus Indexed Journal, Jan 2025.
- I was awarded with “RAMAN RESEARCH AWARD” by VIT, Vellore, on be-half of my Research article published in Scopus Indexed Journal, Feb 2025.
- I was awarded with “RAMAN RESEARCH AWARD” by VIT, Vellore, on be-half of my Research article published in Scopus Indexed Journal, Jan 2024.

Academic Activity

- Acted as a Reviewer for “3rd IEEE International Conference on Artificial Intelligence for Internet of Things (AIIoT 2024)” organized by the School of Electronics Engineering, VIT, Vellore, on May 03-04 ,2024.
- Acted as a Reviewer for “IEEE International Conference on Next Generation Electronics (NEleX-2023)” organized by the School of Electronics Engineering, VIT, Vellore, on Dec 14-16,2023.
- Acted as a AICET Co-Ordinator at the Department level in Mother Theresa Institute of Engineering and Technology, Palamaner, during my working period.
- Acted as an Anti-Ragging Committee & Discipline Committee coordinator in Mother Theresa Institute of Engineering and Technology, Palamaner, during my working period.

Faculty Development Activity/Workshops

- Participated in one day workshop on “AI Tools” organized by Be 10X on 03-12-2023.
- Participated in one day Faculty Development Programme on “The Art of Doing High-Quality Research and Research Publications” organized by School of Electronics Engineering, VIT, Vellore, on 12-04-2022.
- Participated in one day Faculty Development Programme on “Method to increase dependability in a Cloud-Fog-Edge Environment” organized by School of Electronics Engineering, VIT, Vellore, on 15-04-2022.
- Participated in two days Faculty Development Programme on “Multi-objective Optimization for Engineers: Principles, Applications, and Programs” organized by School of Chemical Engineering, VIT, Vellore, from 22-09-2022 to 23-09-2022.

Contact Details

