




DAYANANDA SAGAR ACADEMY OF TECHNOLOGY AND MANAGEMENT
22 Mile, B.M Kaval, Udayapura, Kanakapura Road, Bangalore-560082.

DEPARTMENT OF ELECTRICAL & ELECTRONICS

Faculty Name	RAMYA S RAJAN	
Designation	Asst. Professor	
Educational Qualification	M.Tech	
Experience in Years	4.5 years	
Area of Interest	VLSI & Embedded Systems	
Email ID	ramyasunderrajan@gmail.com	

Educational Details

- M.Tech-VLSI and Embedded Systems, VTU
- B.E- Electrical and Electronics Engineering, VTU

Personal Details

- Hobbies: Coin Collection, Reading books
- Languages Known: English, Kannada, Hindi, Tamil

Professional Experience

- Working as Asst Professor in EEE Department, DSATM from August 2013 to till date

Publications

International Journals:

- NIL

National Journals:

- NIL

Conferences:

- “Solar PV cell interfaced with Grid connected Multilevel Inverter for Distributed Power Generation”, *International Conference on Applied Science Engineering and Technology (ICASET-17)*, at Sri Sairam College of Engineering, Anekal, Bengaluru on 18th and 19th May 2017
- “Hybrid Converter with Simultaneous DC and AC Output”, *Third National Conference on Convergence of Science, Technology and Management(NCCSTM 2017)* at DSATM Bengaluru on 19th May 2017
- “Design and Simulation of Three Phase Multilevel Inverter fed Induction Motor Drive”, *Third National Conference on Convergence of Science, Technology and Management(NCCSTM 2017)* at DSATM on 19th May 2017
- “Soft Start of Single Phase Pump Motor”, *First National Conference on Green Computing Technologies(NCGCT-2015)* DSATM on 6th May 2015
- “Determining Standard Cell Drive Strength Based on On-Chip Load Assessment”, *International Conference on VLSI, Communication, Advanced Devices, Signals & Systems and Networking (VCASAN-2013)* at BNMIT from 17th -19th July 2013
- “Accurate Power measurement methodology for VLSI circuits using CAD tools”, *International Conference on Devices, Circuits and Systems – ICDCS 2012* at Karunya University in collaboration with IEEE on 15th March 2012

- “Study of effectiveness of Circuit level Leakage Power Optimization Techniques in DSM CMOS cells”, *International Conference on Energy and Electrical Systems (ICEES 2011)*, Kuala Lumpur, Malaysia, August 12-14, 2011
- “Design of a 4 bit accumulator using new 45nm low leaky standard cells”, *IP-SoC 2011 Conference*, Grenoble, France, Dec 7-8, 2011

Awards

- “Determining Standard Cell Drive Strength Based on On-Chip Load Assessment” Proceedings of International Conference on VLSI, Communication, Advanced Devices, Signals & Systems and Networking (VCASAN-2013), SPRINGER, BNMIT, Bangalore 17-19 July 2013, pp.117-123 was adjudged “the best research paper” on the occasion of International Conference on VLSI, Communication, Advanced Devices, Signals and Systems and Networking, VCASAN-2013

Academic Activity

- Worked as Placement Co-coordinator, Test Co Co-coordinator, Proctor, Class Teacher
- Member- Career Guidance Placement cell
- Member- Grievances Redressal Cell

Faculty Development Activity

- Attended “TWO WEEK ISTE STTP ON ELECTRICAL POWER SYSTEMS WORKSHOP” from 10/07/2017 - 15/07/2017 organized by IIT Khargpur at BMS College of Engineering
- Participated in National workshop on “Topical and Emerging Smart grid Technology” held during 6th November 2015 at DSATM, Bangalore.
- Two day workshop on “Industrial automation –PLC & SCADA” in association with Innovians Technologies at DSATM on 25th and 26th April 2014.
- 3 days training program on Power system simulation studies using MI power software held by PRDC Bangalore from 16th to 18th October 2014 at Dayananda Sagar Academy of technology and Management, Bangalore

Administrative Activity

- Nil

Professional Membership

- Nil

Contact Details

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My Vision

- To continuously strive to develop my knowledge, skills, and competence to better fulfill education, research, and service responsibilities of myself as well as my students