




DAYANANDA SAGAR ACADEMY OF TECHNOLOGY AND MANAGEMENT

(Affiliated to Visvesvaraya Technological University, Belagavi & Approved by AICTE, New Delhi)

Opp. Art of Living, Udayapura, Kanakapura Road, Bangalore- 560082

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

(Accredited by NBA, NAAC with A+, New Delhi)

Faculty Name	Mr. Mahadev S.	
Academic Designation	Assistant Professor	
Educational Qualification	BE, M. Tech.	
Experience in Years	16.7	
Area of Interest	VHDL, Verilog, Digital Electronics, Digital signal Processing, Electromagnetic Waves, and Microprocessor.	
Date of Birth	14-02-1982	
Email ID	mahadev-ece@dsatm.edu.in mahadevkhanderao@gmail.com	

Educational Details

- M.Tech (VLSI Design & Embedded Systems), VTU, Belagavi 2007.
- B.E (Electronics & Communication Engineering) VTU, Belagavi, 2003.

Personal Details

- Date of Birth: 14/02/1982
- Hobbies: Reading Novels, Writing Articles, Writing Shayaris, Motivational Speaker and Playing Outdoor Games.
- Languages Known: Kannada, English & Hindi.

Professional Experience

- Working as Assistant Professor in department of Electronics and Communication Engineering, Dayananda Sagar Academy of Technology and Management, Bengaluru from 10-03-2023 to till date
- Worked as an Lecturer, Senior Lecturer & Assistant Professor in department of Electronics and Communication Engineering, Ghousia College of Engineering at Ramanagara from 06-03-2008 to 09-03-2023.
- Worked as Teaching Assistant in Department of ECE, at PDA College Of Engineering Gulbarga from 23rd August 2004 to 17th September 2005.
- Worked as Member Technical Staff In Sandeepani School of VLSI Design a branch of Cg-Coreel, in Hyderabad from 16th July 2007 to 25th January 2008.

- Worked as Trainee in Mind Tree Consultancy, Bangalore from 24th, July 2006, to 11th June 2007.

Publications

International Journals:

- Centralized Traffic Management System Using Computer vision at European Chemical Bulletin 2023, Vol. 12, 3984-399. DOI : 10.31838/ecb/2023.12.si6.353.
- Customized Mask R-CNN for Un-Uniformed shape text Detection and text recognition at International Journal of electrical and Computer Engineering, Indonesia 2022.

International Conferences:

- An Algorithm to Compute the Number of Repetitions and its Iterations from Stuttered Speech for Objective Assessment of Stuttering, at International Conference ICOICT, Trivandrum, Kerala, India, February 2009, pp. 147-150.

National Conferences:

- Synthesis and Functional Verification of High Speed UART for Bluetooth Application at National Level conference on Advancement in Communication and Computation organized by KSIT, Bangalore on September 28th 2013.

Faculty Development Program Attended

- “Advance Control Engineering and Simulation Using Matlab and Simulink Tools” Organized by department of Electrical and Electronics Engineering, Ghousia college of Engineering, Ramanagaram, from 26th August 2019 to 31st August 2019.
- “Entrepreneurship” Organized by National Institute of technical teachers training and research, Chennai, in association with National Science and technology Entrepreneurship development board department of science and technology, Government of India, New Delhi from 21st January 2019 to 02nd February 2019.
- “Micro And Nano Electronics (MAN)” at Guru Nanak Dev Engineering, Bidar, from 06th August 2013 to 09th August 2013.
- “Mission10X” in Ghousia College of engineering, Ramanagaram, from 21st March 2011 to 25th March 2011.
- “VLSI Design and FACE Program”, Organized by REVA Institute of Technology and Management in Bangalore on 23rd July to 30th July 2007.
- “Soft Skills and Efficiency Enhancement Skills”, Organized by S.J.C.E Mysore on 7th July to 23 July 2005.

Seminar Attended

- Participated in the webinar on “Project management” organized by PM primer, Bangalore, on 12th September 2015
- Participated in the seminar on ‘Exploring Information Sources for Research and Development’, organized by JSS Academy of Technical Education, at Bangalore on 12th October 2012.
- Participated in the seminar on Controller Area Network” organized by ISM, Bangalore on 16th May 2009.

Workshop Attended

- Participated in One day FDP on “Effective e-Resources Retrieval Strategies for Efficient Research” at GCE, Ramanagaram on 9th Feb.2019.
- Participated in Three day's workshop on “Signal Processing with Matlab and simulink” held at Ghousia college of Engineering, Ramanagaram, from 21st March 2013 to 23rd March 2013.
- Participated in a One day workshop on “Teaching-Learning Conclave” held at Dayananda Sagar College of engineering, Bangalore on 19th August 2011.
- Participated in a One day workshop on “Cadence Based Digital VLSI Design Flow” on 6th of March 2010, organized by New Horizon college of Engineering Bangalore in association with Indian Institute of VLSI design and training, Bengaluru.
- Participated in a Two days’ workshop on “Nanometer VLSI Design Methodologies” organized by VTU at RV VLSI Design Centre, Bangalore on 6th and 7th October 2009.

Patents published with All Details

Title: An Automated Physiotherapeutic Machine for Orthopedic and Paralysis treatment

Application No. : 202241015292 A

Date of Publication: 25-March-2022

Country: India

Professional Membership

- Life member of Indian Society For Technical Education LM77815
- Life member of Institute of Engineers M-1588189.

Student Project Funds Fetched with Grant No.

- An amount of Rs. 2,40,000/- fund received for guiding the students project on “An Automated Physiotherapeutic Machine for Orthopedic and Paralysis treatment” at Ghousia College of Engineering, under New Age Incubation Center (NAIN) scheme of Government of Karnataka Academic Year 2019-20.

Awards

- I was awarded with “Best Paper of the session” for the paper presentation at National level conference organized by KSIT Bangalore, on September 28th 2013.

Academic Activity

- Class Coordinator,
- Department Placement Coordinator
- Worked as Cultural Coordinator for the department
- Worked as Department Test Coordinator
- Assisted in organizing Faculty Development program.
- NBA Work: Part B, Student Performance chapter 4 of Self-Assessment Report of NBA.
- NAAC work: result and placement details

Faculty Development Activity/Workshops

- Participated in Online One Week AICTE–VTU Joint Teachers Training Programme on “Electronics Circuit design using Lab View” Organized by Visvesvaraya Technological University, Centre for PG Studies, VIAT, Muddenahalli, Chikkaballapur (Dist.), from 12th to 16th June 2023
- Participated in Online FDP on "Machine Learning and Data Analytics using MATLAB" organized by Kalpataru Institute of Technology, Tiptur, from 24th April -29th April 2023
- Participated in Online FDP on Recent trends in materials and flexible electronics" Organized by the Department of Electronics and Communication Engineering of Techno College of Engineering, Agartala from the 10 April to the 14 April 2023.
- Participated in ATAL Academy Online Advanced FDP on “Artificial Intelligence and Blockchain Application for Food Agriculture Sector” organized by Indian Institute of Plantation Management, Bengaluru, from 24th to 28th Jan 2022.
- Participated in ATAL Academy Online Elementary FDP on “Machine Learning Applications on Image Processing” organized by Nagarjuna College of Engineering & Technology, Bengaluru, from 17th to 21st Jan 2022.
- Participated in Five days online National Level FDP on “PCB Design” organized by Dept. of ECE, VVIET, Mysuru in association with Sienna ECAD technologies and ESSCI, from 29th June to 03rd July 2020.
- Participated in One-week online FDP on “Designing Automation for Future Industries” organized by Dept. of ECE, Ghousia College of Engineering, Ramanagara, from 9th to 13th June 2020.
- Participated in One day FDP on “Effective e-Resources Retrieval Strategies for Efficient Research” organized by Library and Information Centre in collaboration with IEEE, Ghousia College of Engineering, Ramanagaram on 9th Feb 2019.
- Participated in AICTE Sponsored two weeks SDP on “Recent Developments in Renewable Energy Sources & Its Applications” organized by Dept. of Mechanical Engineering, Ghousia College of Engineering, Ramanagaram from 13th to 25th Nov 2017.
- Participated in two days FDP on “Research Proposal Preparation Towards Ph.D. admission Programmes” organized by Dept. of ECE, KSSEM, Bangalore on 25th & 26th Jul 2017.
- Attended FDP on “Cloud Computing, Digital Learning, Artificial Intelligence & NPTEL” organized by iON PRIDE, TCS Global Axis, Bengaluru on 23rd & 24th Jan 2016.
- Attended FDP on “VLSI Design and Intellectual Property” organized by Dept. of ECE, Jyothi Institute of Technology, Bangalore on 9th & 10th Jan 2015.
- Attended FDP on “Advances in CMOS VLSI Design” organized by PESCE, Mandya sponsored by VGST from 8th Feb to 10th Feb 2011.

Workshops Organized

- Organized one day Technical talk on “MATLAB DAY” in association IEEE Student chapter along with CoreEL Technologies, Bengaluru and ECE department DSATM, Bengaluru (08-08-2023).
- Organized three days’ workshop on “Introduction to Image processing and AI workflow for engineering Application with MATLAB” in Association with CoreEL Technologies, Bengaluru and DSATM, Bengaluru (03-05-2023 to 05-05-2023).
- Organized Five Online FDP on “Designing Automation for Future Industries” in department of ECE, Ghousia College of Engineering, Ramanagara from 09-06-2020 to 13-06-2020).

Contact Details

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