




Faculty Name	ROOPA K R	
Academic Designation	Assistant Professor	
Educational Qualification	B. E., M.Tech.,(Ph.D)	
Experience in Years	15 years	
Area of Interest	VLSI TESTING	
Date of Birth	30th October 1985	
Email ID	roopakr.ec07@gmail.com	

Educational Details

M.Tech in VLSI AND EMBEDDED SYSTEMS from JNTUH

Personal Details

Email Id: roopakr.ec07@gmail.com

Professional Experience

Worked as Assistant Professor in Electronics and communication Department, RNS Institute of Technology, from March 2022 to August 2024.

Worked as Assistant Professor in Electronics and communication Department, DBIT, from April 2008 to Feb 2022.

Publications

1. Presented a paper at an International conference on Recent Trends in Computational Engineering and Technologies ICRTCET-2024 titled “Guarding the Grid: Exploring Advanced Strategies for Detecting Intrusions in Power Distributed Devices” organized by RRCE Bangalore on 25 th & 26 TH July 2024.
2. Presented a paper in the Taylor & Francis Group Scopus Indexed International Conference titled “Low Power Technique to test VLSI Circuit” – International Conference on Computing and Intelligent Information Systems at BNMIT during April 19- to 20 2024.
3. Published a paper in a National conference titled “Design and Analysis of Polynomial based TPG’s for varying word lengths” held at RNSIT on 30 th April 2024 Organized by Dept of ECE -IETE seventh National Conference on Emerging Trends in Engineering Science and Technology.

4. Published a paper in a National conference titled “Design of 16 bit Kogge-stone adder using GDI Technique ” held at RNSIT on 30 th April 2024 Organized by Dept of ECE - IETE seventh National Conference on Emerging Trends in Engineering Science and Technology.
5. Published a paper in Springer book chapter “Brain Tumor Detection and classification” -International Conference on Paradigms of Communication, computing and Data Analytics- PCCDA will be published by September ebook ISBN: 9789819946259.
6. Published a paper in Springer book chapter “Smart Street lightening using Piezoelectric Technique” -Proceedings of the International Conference on Cognitive and Intelligent Computing. Ebook ISBN 9789811923500.
7. ROOPA.K.R presented a Technical paper entitled “An Extremely Low Power 2ghz Cmos LC Vco For Wireless Communication Application” in National Conference during 2011 held at Electrical & Electronics Department, DBIT.
8. ROOPA.K.R, JISHA.P.P presented a Technical paper entitled “Design of Ultra Low Power circuit & Considerations for Ultimate Cmos scaling ” during 2012 National Conference held at Electrical & Electronics Department, DBIT.
9. ROOPA.K.R presented a Technical paper entitled “A Novel Cryptographic Technique Using Hash Functions” for NCRTC during 2013 held at Tele Communication Department, DBIT.
10. ROOPA.K.R presented a Technical paper entitled “Wireless Communication through Intel X-Scale Application Platform” for NCRTC during 2013 held at Tele Communication Department, DBIT.

Conference Attended

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Awards

1. Got First place for the paper presented at National Conference-RNSIT titled “Design and Analysis of Multi bit test pattern generation with Dynamic LFSR selection.”
2. Secured First place for the paper presented in International Conference in RRCE Bangalore.

Faculty Development Activity/Workshops

1. Attended 5 Day Virtual workshop on “VLSI to System Design : Silicon to end Organized by AICTE ,from 31 st July to 4th.
2. Completed 1 week AICTE-VTU Joint Teachers Training Programme on” Analog VLSI Design using EDA Tools”. from 20 th to 25 th March 2023.
3. Completed Training Program in Online mode on “VLSI DIGITAL LABORATORY PRIMER”. Organized by ENTUPLE TECHNOLOGIES, on Nov 08 th 2021 to Dec 03 2021.
4. Completed Training Program in Online mode on “VLSI ANALOG LABORATORY PRIMER”. Organized by ENTUPLE TECHNOLOGIES, on Oct 28 th 2021 to Nov 17 th 2021.

Contact Details

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